

## AFFIDAVIT

At the prompting of the examiner all additions or deletions were made for clarity. No such changes affected device function. Thus, they raised no new issues. Further search and consideration is not needed.

The principle behind device function is herein stated concisely in the specification and the Claims. In the Cartesian plane at the intersection of the maximum or minimum voltage tracing with the desired entrance voltage, the abscissal value of that intersection is the exit voltage, which maintains the entrance voltage in its desired range. The examiner was unaware of this at our last communication until it was clarified.

The specification and Claims are herein made more definite and do not affect device function.

The error in the equation of MINR has been previously corrected to  $y=1/x$ .

The tracing remained the same.

